

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,844,601 B2
APPLICATION NO. : 10/000479
DATED : January 18, 2005
INVENTOR(S) : Jigish D. Trivedi and Michael P. Violette

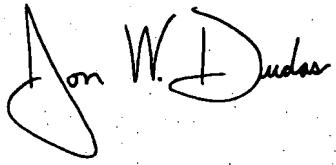
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 6, LINES 44-45, change "interconnect 32" to --interconnect structure
32--

Signed and Sealed this

Sixth Day of November, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office